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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/648,853

08/26/2003

Kazushige Kanda

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3862

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HOGAN & HARTSON L.L.P.

500 S. GRAND AVENUE

SUITE 1900

LOS ANGELES, CA 90071-2611

EXAMINER

TRAN, MICHAEL THANH

ART UNIT

PAPER NUMBER

2827

DATE MAILED: 03/08/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/648,853

Applicant(s)

KANDA ET AL.

Examiner

Michael t. Tran

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on August 26, 2003 through January 26, 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 8-15, 20 and 21 is/are allowed.
- 6) ☒ Claim(s) 1 and 16 is/are rejected.
- 7) ☒ Claim(s) 2-7 and 17-19 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 00105.

- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

MICHAEL TRAN

DETAILED ACTION

1. In response to the Communications dated August 26, 2003 through January 26, 2004, claims 1-21 are active in this application.

Foreign Priority

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a) (d), which papers have been placed of record in the file.

Information Disclosure Statement

3. The information disclosure statements filed August 26, 2003 and January 15, 2004 have been considered.

Claim Objections

4. Claims 2-7 and 17-19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

It appears that the word —to—should be inserted between the words “according” and “the” of line 15 and the last line of claim 21.

Claim Rejections – 35 U.S.C. § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102

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that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in-

(1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or

(2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).

6. Claim 1 is rejected under 35 U.S.C 102(b) as being anticipated by Miyakawa et al. [U.S. Patent #6,088,281].

With respect to claim 1, Miyakawa et al. disclose a non-volatile semiconductor storage device comprising: a ROM region which stores fuse data [30 of figure 1]; at least one pad to which a control signal is supplied [any node can be interpreted as being a pad]; and a read control circuit [figure 2] connected to the ROM region and the at least one pad to receive a power voltage [electrode of PON of figure 2] and control reading of fuse data [via RSTART of figure 2] from the ROM region after the power voltage has reached a predetermined level during a rise so as to control timing for activating an operation of reading the fuse data, according to the control signal.

7. Claim 16 is rejected under 35 U.S.C 102(b) as being anticipated by Miyakawa et al. [U.S. Patent #6,088,281].

With respect to claim 16, Miyakawa et al. disclose a non-volatile semiconductor storage device comprising: a ROM region which stores fuse data [within 30 of figure 1]; a pulse generating circuit [33 of figure 2] which generates a pulse signal [FSELB] on the basis of a first signal supplied to a first pad [any of the electrode coupled to 33 of figure 2]; a delay circuit [30b of figure 1 has a delay circuit - 36] which receives and delays the pulse signal and has delay time controlled on the basis of a second signal [FLST of figure 2] supplied to at least one second pad; and a ROM read control circuit [figure 2] connected to the ROM region and the delay circuit to control reading of the fuse data from the ROM region according to an output from the delay circuit.

Allowable Subject Matter

8. Claims 8-15, 20 and 21 are allowable over the prior art of record.

9. The following is an Examiner's statement of reasons for the indication of allowable subject matter: the prior art of records does not show (in addition to the other elements in the claim) the following:

- A read control circuit connected to the ROM region to receive the first fuse data and control reading of the second fuse data from the ROM region after the power voltage has reached a second level during a rise so as to control timing for activating an operation of reading the second fuse data, according to the first fuse data.
- At least two second non-volatile memory chips having a ROM region which stores fuse data, the at least two second non-volatile memories each receiving

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the power voltage and controlling reading of the fuse data from the ROM region after the power voltage has reached a predetermined level during a rise, the at least two second non-volatile memory chips having timings for activating an operation of reading the fuse data which timings are different from that for the first non-volatile memory chip and from each other.

- A second ROM read control circuit which receives an output from the second delay circuit to control reading of the fuse data from the second ROM region according to the output from the second delay circuit.

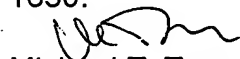
Conclusion

10. When responding to the Office action, Applicants are advised to provide the Examiner with line and page numbers of the application and/or references cited to assist the Examiner in the prosecution of this case.

11. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Michael T. Tran whose telephone number is (571) 272-1795. The Examiner can normally be reached on Monday-Thursday from 7:30-6:00 P.M.

12. Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (571) 272-

1650.


Michael T. Tran
Art Unit 2827
March 1, 2005

MICHAEL TRAN
EXAMINER